

L Number	Hits	Search Text	DB	Time stamp
-	35	(adachi.in. and fuji.as.) and (ic (integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 09:10
-	96	((high-speed low-speed) adj (access device)) same control\$4 same switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:35
-	92	high-speed same low-speed same access same switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:19
-	1070	segmented adj3 (bus line path)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:26
-	40	(segmented adj3 (bus line path)) same switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:27
-	48	(data adj (bus transmission path)) with priority with speed same control\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:31
-	7419	(transmission adj line) same control\$4 same switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:39
-	9	(transmission adj line) same control\$4 same switch\$3 same access same priority	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:42
-	0	bus same swith same control	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:44
-	23891	bus same switch same control	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:44
-	52	bus same switch same control same priority same access same (device\$1 peripheral\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:48
-	0	variable with clock with bus with control adj switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:48
-	3	variable with bus with control adj switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:53

-	4	variable with bus with (operating adj speed)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:55
-	503	bus with (operating adj speed)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:58
-	6	(bus with (operating adj speed)) same priority	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:56
-	93	bus with (operating adj speed) with device\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 13:58
-	116	slowest adj device\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 14:14
-	110	(bus (transmission adj line)) same ((plural multiple) near speed)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 14:53
-	466	(bus (transmission adj line)) same ((plural multiple) near3 speed)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 14:53
-	100	(high-speed same low-speed) adj3 device	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 16:45
-	6	((high-speed same low-speed) adj3 device) same priority	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 16:45
-	6	((high-speed same low-speed) adj3 device) same (priority precedence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 16:46
-	2	high-speed same low-speed same access same switch\$3 same priority	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/20 14:25
-	1052	adachi.in. and fuji.as.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:21
-	275	(adachi.in. and fuji.as.) and (switch\$3 control\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:22

-	0	(kaoru.in same adachi.in.) and fuji.as.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:21
-	48	(kaoru.in. same adachi.in.) and fuji.as.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:21
-	16	((kaoru.in. same adachi.in.) and fuji.as.) and (switch\$3 and control\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:22
-	37	((kaoru.in. same adachi.in.) and fuji.as.) and (switch\$3 control\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:24
-	19287	bus same (disconnect\$3.off) same (connect\$3 on)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:32
-	2762	bus same (disconnect\$3 off) same (connect\$3 on) same (switch same control)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:32
-	269	bus same (disconnect\$3 off) same (connect\$3 on) same (switch same control) same speed	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/19 17:33
-	16	common adj bus adj arbiter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/20 16:33
-	2	(common adj bus adj arbiter) same priority	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/20 15:37
-	35	((multiple plural plurality) near3 switch\$3) with priority with access	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/21 16:55
-	8	connect\$3 same disconnect\$3 same switch\$3 same priority same bus same access	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 11:00
-	184	((connect\$3 disconnect\$3) same switch\$3 same priority same bus same access) not (connect\$3 same disconnect\$3 same switch\$3 same priority same bus same access)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 11:00
-	8058	varying adj3 pulse\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 12:23

-	0	(varying adj3 pulse\$1) same bus same control\$4 same swith\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 12:23
-	22	(varying adj3 pulse\$1) same bus same control\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 12:26
-	1367	varying adj pulse adj width	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 12:25
-	6	(varying adj pulse adj width) same bus same control\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/22 12:40
-	2	data-transfer adj enable adj signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 11:18
-	62	data adj transfer adj enable adj signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 11:51
-	0	(data adj transfer adj enable adj signal) same Varying	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 11:50
-	3	(data adj transfer adj enable adj signal) same width	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 11:50
-	12668	chang\$3 with (pulse adj3 width)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 11:59
-	4368	varying with (pulse adj3 width)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 11:59
-	1395	varying adj (pulse adj3 width)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 11:59
-	1370	varying adj (pulse adj width)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 12:00
-	2	varying adj (clock adj pulse adj width)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 12:00

-	2	(varying adj (pulse adj width)) same (data adj bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 12:02
-	9	(varying adj (pulse adj width)) same bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 12:43
-	111	(chang\$3 near3 (pulse adj width)) same (enable disable)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 12:44
-	269	enabl\$3 with fixed with time with (elapsed wait\$3 pass\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:01
-	0	(enabl\$3 with fixed with time with (elapsed wait\$3 pass\$3)) same bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:01
-	12	bus same pulse same width same variable same enable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:04
-	26	(chang\$3 adj3 (pulse adj width)) same ((data transmission) adj (bus line))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:20
-	19	(variable adj pulse adj width) same switch\$3 same control\$4 same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:23
-	2518	changed adj frequenc\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:26
-	21	(changed adj frequenc\$3) same bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:26
-	288	(changed adj frequenc\$3) same clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:27
-	7	((changed adj frequenc\$3) same bus) same ((changed adj frequenc\$3) same clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:27
-	1	((high-speed low-speed) adj (access device)) same control\$4 same switch\$3 same (sync synchronized)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:36

-	0	((high-speed low-speed) adj (access device)) same control\$4 same switch\$3 same (pulse adj width)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:37
-	22	((high-speed low-speed) adj (access device)) same control\$4 same switch\$3 same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:37
-	85789	clock adj puls\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:41
-	282	(clock adj puls\$2) same (data adj transfer) same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:41
-	433	(clock adj puls\$2) same (data adj transfer\$4) same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:41
-	78	(clock adj puls\$2) same (data adj transfer\$4) same enabl\$3 same (different chang\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:42
-	82	(clock adj puls\$2) same (data adj transfer\$4) same enabl\$3 same (different chang\$3 variable varying)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 13:42
-	101	switch\$3 same (open disconnect\$3) same (close connect\$3) same (wait\$3 delay\$3 stable) same bus same (pulse speed frequenc\$3 width\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 14:10
-	0	(dynamic adj chang\$3) with (pulse adj width) with (data adj bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 14:17
-	0	(dynamic adj chang\$3) with (pulse adj width) with bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 14:17
-	1	(dynamic adj chang\$3) with (pulse adj width) with clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 14:18
-	0	(dynamic adj chang\$3) with (pulse adj width) with switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 14:19
-	8	(dynamic adj chang\$3) with (pulse adj width)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 14:19

-	306	dynamic with chang\$3 same frequency same switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/23 14:46
-	569	sync same clock same switch same frequency	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 09:41
-	89	sync same clock same switch same frequency same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 09:44
-	0	sync same clock same switch same (pulse\$1 with width with chang\$3) same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 09:47
-	2	sync same clock same switch same (pulse\$1 with width with chang\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 09:47
-	46	PCI same clock same chang\$3 same switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 09:48
-	56	switch\$3 with frequency with enabl\$3 with transfer\$4	USPAT	2003/10/24 10:22
-	238	data with transfer\$4 with chang\$3 with frequency	USPAT	2003/10/24 10:27
-	16	data with transfer\$4 with chang\$3 with frequency with pulse\$1	USPAT	2003/10/24 10:27
-	5489	bus same clock same chang\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 11:24
-	30	switch same (bus adj control) same enabl\$3 same transfer\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 11:54
-	2	6519669.pn. and switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 12:24
-	2	6519669.pn. and (transfer\$4 switch\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 12:25
-	2	6519669.pn. and (transfer\$4 switch\$3 enabl\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 12:25
-	595	(delay adj (logic circuit)) same switch same transfer\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 14:03
-	39	(delay adj (logic circuit)) same switch same transfer\$4 same bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 14:03

-	109	circuit same enable same transfer same switch same connection	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 14:12
-	1041	var\$5 with (clock adj3 cycle)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 15:20
-	0	(var\$5 with (clock adj3 cycle)) same bus same switch same transfer\$4 same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 15:19
-	0	(var\$5 with (clock adj3 cycle)) same switch same transfer\$4 same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 15:19
-	1	(var\$5 with (clock adj3 cycle)) same switch same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 15:19
-	52	(var\$5 with (clock adj3 cycle)) same width	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 15:20
-	44108	var\$4 adj3 frequency	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 15:33
-	69	(var\$4 adj3 frequency) same enabl\$3 same transfer\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 15:33
-	2023755	(varying variation varied) clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 15:53
-	25305	(varying variation varied) same clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 15:53
-	6	(data adj transfer\$4) same clock same settle same chang\$3 same (width pulse frequency)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 10:35
-	1	swith same chang\$3 same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 10:41
-	1	?m66en same prsnt\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 11:10

-	76	prsnd\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 11:10
-	1	?m66en and prsnd\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 11:10
-	5	m66en same prsnd\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 11:10
-	744	switch same transfer same start\$3 same enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:13
-	260	(switch same transfer same start\$3 same enabl\$3) same (clock pulse)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:18
-	27746	enabl\$3 adj3 transfer\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:18
-	10	(chang\$3 near3 frequency) same (enabl\$3 adj3 transfer\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:21
-	85794	clock adj pulse\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:28
-	82	(clock adj pulse\$1) SAME (VARIABLE ADJ WIDTH)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:29
-	8	(clock adj pulse\$1) SAME (VARIABLE ADJ WIDTH) same switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:30
-	0	(clock adj pulse\$1) SAME (dynamic ADJ WIDTH) same switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:30
-	143	(clock adj pulse\$1) SAME dynamic same switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:30
-	25	(clock adj pulse\$1) SAME dynamic same chang\$3 same switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/27 16:30

-	1180	(713/400\$ 713/500\$).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/03 13:39
-	42	((slow (high fast)) adj speed) adj3 (device peripheral)) same clock same switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/03 13:36
-	0	((slow same (high fast)) adj speed) adj3 (device peripheral)) same clock same switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/03 13:36
-	0	switch\$3 same contrl\$4 same frequency same (delay elapsed wait\$3) same stable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/03 13:48
-	2464	frequency with chang\$3 with stable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/04 15:50
-	147	(frequency with chang\$3 with stable) with switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/04 15:50
-	732	713/400\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/04 15:54
-	133	switch\$3 same ((slow same fast) adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/04 16:09
-	199854	(variable variation varying variant) with (clock\$1 pulse\$1 frequency)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/04 16:24
-	10	(power with sav\$3) same clock same (delay\$3 wait\$3 elapsed) same stable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 09:33
-	32	speed same clock same (delay\$3 wait\$3 elapsed) same stable same chang\$3 same frequency	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 09:34
-	1	(clock adj pulse\$1) with width with (variant varying variable) with (modify\$3 chang\$3) with enabl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 14:31
-	15	(clock adj pulse\$1) with width with (variant varying variable) with (modify\$3 chang\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 13:51

-	7	(data adj transfer) same enabl\$3 same control\$4 same stabl\$3 same (elapsed delay wait\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 14:25
-	0	((different adj domain) with clock\$3) same (sync synchroniz\$5) same elapsed same (fixed adj3 (period duration time))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 14:28
-	0	((different adj domain) with clock\$3) same (sync synchroniz\$5) same elapsed same (period duration time)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 14:31
-	0	((different adj domain) with clock\$3) same (sync synchroniz\$5) same elapsed	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 14:28
-	1	((different adj domain) with clock\$3) same (sync synchroniz\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 14:29
-	4	((different adj domain) with clock\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 14:30
-	0	((different adj domain) with clock\$3) same switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 14:30
-	19	(different adj domain) same switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/05 14:31
-	722	(enabl\$3 with transfer\$4) same clock same (different chang\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 09:22